

Appl. No. 10/754,726
Reply to Office Action of July 12, 2005

Remarks

Applicants respectfully request reconsideration and allowance in view of the foregoing amendments and following remarks. In the Office Action, mailed July 12, 2005, the examiner rejected claims 1-5, 13-23 and objected claims 6-12. By this amendment, claims 1-23 have been canceled, without prejudice, and new claims 24-43 have been added. Following entry of these amendments, claims 24-43 will be pending in the application. No new matter is entered.

Claim Rejections under U.S.C. § 112

In the Office Action, the Examiner rejected claims 14-17 and 19-23 under 35 U.S.C. 112, second paragraph, as being indefinite for particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Applicant has canceled claims 14-17 and 19-23, without prejudice, rendering the Examiner's rejections moot. Thus, for at least this reason, Applicant respectfully requests withdrawal of this § 112 rejections.

Claim Rejections under U.S.C. § 102(e)

In the Office Action, the Examiner rejected claims 1-5, 13 and 18 under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,566,933 to Lye. (hereinafter "Lye"). The Examiner also rejected claims 14-22 under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,788,116 to Cook et al. (hereinafter "Cook"). The

Appl. No. 10/754,726
Reply to Office Action of July 12, 2005

examiner further rejected claims 19-21 and 23 under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,720,805 to Hass. (hereinafter "Hass").

Applicant has canceled claims 1-5, 13-23, without prejudice, rendering the Examiner's anticipation rejections moot. Thus, for at least this reason, Applicant respectfully requests withdrawal of this § 102(e) rejections.

New Claims

Applicant has added claims 24-43. Applicant asserts that the new claims are in condition for allowance.

Lye shows an output circuit (70, 72) which is connected to two current sources (I1 and I2) and receives currents of the current sources and produces a differential signal. Cook also shows an output circuit (M1, M2, M3, and M4) which is connected to a current source (M0) and receives a current (Id) of the current source (M0) and produces an output signal (Vout). Haas also shows an output circuit (M1, M2, M3, and M4) which is connected to a current-mirror (M7) and receives a current (IOUT) of the current source (M7) and produces an output signal (OUTP, OUTN).

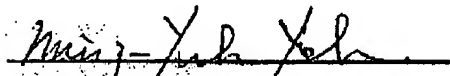
Lye, Cook, and Hass fail to disclose the output circuit is directly coupled to the operational voltage (VDD) and to ground (VSS/GND). Further, Lye, Cook, and Hass fail to disclose a magnitude of the differential signal is determined based on at least one of a difference of the operational voltage and the control voltage and a difference of the control voltage and the ground. Further, Lye, Cook, and Hass fail to disclose while the output circuit outputs the differential signal, the at least one of the first, the second, the third, the fourth transistors of the output circuit operates at a saturation region.

Appl. No. 10/754,726
Reply to Office Action of July 12, 2005

Therefore, for at least these reasons, claims 34-43 are in condition for allowance.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Sincerely yours,


Ming-Yuh Yeh, Patent Counsel

Date: 2005. Dec. 9